

Grinding Parameter Designs For Wafer Grinding With Predesignated Wafer Profiles

**Tzu-Fan Chiang¹, Sheng-Min Guo¹, Wen-Nan Cheng¹, Chih-Chun Cheng¹, Yu-Hsin Kuo¹,
Chien-Sheng Liu², Cheng-Kuo Sung³**

¹Advanced Institute of Manufacturing with High-tech Innovations and Department of Mechanical Engineering/National
Chung Cheng University

No.168, Sec. 1, University Rd., 621, Min-Hsiung Town-ship, Chia-yi County, Taiwan, R.O.C.

d08420001@ccu.edu.tw; sa3040340@gmail.com; wennan@ccu.edu.tw;

imeccc@ccu.edu.tw; astkyh2@ccu.edu.tw

²National Cheng Kung University

No. 1, University Road, East District, Tainan, Taiwan, R.O.C.

cslu@mail.ncku.edu.tw

³Department of Power Mechanical Engineering/National Tsing Hua University

Engineering Building 1, No. 101, Section 2, Kuang-Fu Road, Hsinchu, Taiwan, R.O.C.

cksung@pme.nthu.edu.tw

Extended Abstract

The wafer profile plays an important role in determining the wafer quality in semiconductor industry. The wafer quality typically quantified in terms of bow, warp, total thickness variation (TTV) [1] and roughness is strongly affected by the process and setup parameters in a grinding process. The process parameters characterized by the grinding dynamics including the feed rate, the rotational speeds of grinding wheel and chuck table affect the wafer surface roughness. The setup parameters represented by the relative position of the grinding wheel and chuck table, consisting of the tilt angle of the chuck table, the grinding wheel size, and the distance between the grinding wheel and the chuck table determine the resulting wafer profile, such as TTV. On the other hand, the wafer bow, and warp are influenced not only by the setup parameters but also the residual stress during the process.

In this study, a novel methodology in determining optimal grinding parameters for wafer grinding to produce predesignated wafer profile was proposed. The approach is a typical inverse problem which starts with the effects and then calculates the causes. The methodology comprises two phases. In the first phase, a three-dimensional mathematical model which simulates the abrasive trajectories caused by the grit of grinding wheel onto the wafer was created based on homogenous coordinate transformation. This technique was also used in previous research [2, 3]. The abrasive trajectories become be the cross-hatch grinding marks on the wafer surface. The resulting wafer profile after the grinding process are superimposed by those abrasive trajectories and can be predicted accurately after experimental validations. The tilt angles in [4, 5] were found to be the main factor of wafer geometry quality in terms of TTV. In addition, simulations revealed that the main parameters influencing wafer geometry are dominated by the relative position of the grinding wheel and chuck table, including offset distance. With this wafer grinding model, various wafer profiles can be generated by superimposing all the abrasive trajectories while adjusting the tilt angles and offsets, and those simulations serve as training data for the second phase of the proposed methodology. In the second phase, a wafer profile is defined firstly according to the predesignated wafer thickness variation. Then the optimal grinding parameters to produce this predesignated wafer profile based on supervised machine learning was developed with the training data from numerical simulations according to the mathematical model developed in the first Phase. In particular, the labelled data used in the machine learning was created from simulations of the validated mathematical model instead of time-consuming experiments. Results show that the wafer with predesignated wafer profiles can be realized more systematically, enhancing better wafer production efficiency and quality.

References

- [1] I. Kao and C. Chung, *Wafer manufacturing: shaping of single crystal silicon wafers*. John Wiley & Sons, 2021.
- [2] L. Zhou, J. Shimizu, K. Shinohara and H. Eda, "Three-dimensional kinematical analyses for surface grinding of large scale substrate," *Precis. Eng.*, vol.27, no. 2, pp. 175-184, 2003.
- [3] W. Sun, Z. J. Pei and G. R. Fisher, "Fine grinding of silicon wafers: a mathematical model for the wafer shape," *Int. J. Mach. Tools Manuf.*, vol. 44, no. 7-8, pp. 707-716, 2004.
- [4] B. Jiang, D. Zhao, Y. Liu and X. Lu, "Surface figure control in fine rotation grinding process of thick silicon mirror," *Int. J. Adv. Manuf. Technol.*, vol. 98, pp. 771-779, 2018.
- [5] Y. Liu, H. Tao, D. Zhao and X. Lu, "An Investigation on the Total Thickness Variation Control and Optimization in the Wafer Backside Grinding Process," *Materials*, vol. 15, no. 12, pp.4230, 2022.